

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
1	BRS	L1	18279	(micro near mirror) or dmd or mems	USPA T; US-P GPUB ; EPO; JPO; DERW ENT; IBM _TDB	2004/09/0 3 10:29	
2	BRS	L2	3400	circuit\$3 near4 (second near surface)	USPA T; US-P GPUB ; EPO; JPO; DERW ENT; IBM _TDB	2004/09/0 3 10:31	
3	BRS	L3	35	1 and 2	USPA T; US-P GPUB ; EPO; JPO; DERW ENT; IBM _TDB	2004/09/0 3 10:35	
4	BRS	L4	1024	substrate near4 (second near side) same circuit\$3	USPA T; US-P GPUB ; EPO; JPO; DERW ENT; IBM _TDB	2004/09/0 3 10:37	

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
5	BRS	L5	31	1 and 4	USPA T; US-P GPUB ; EPO; JPO; DERW ENT; IBM TDB	2004/09/0 3 10:37	